

Day : Wednesday

**PALM INTRANET**

Date: 5/16/2007

Time: 07:53:48

## Application Number Information

Application Number: **10/593141**

### Assignments

Filing or 371(c) Date: **00/00/0000** eDan

Effective Date: **09/18/2006**

Application Received: **09/18/2006**

Patent Number:

Issue Date: **00/00/0000**

Date of Abandonment: **00/00/0000**

Attorney Docket Number: **5367-264PUS**

Status: **19 /APPLICATION UNDERGOING PREEXAM PROCESSING** Status Date: **09/26/2006**

Confirmation Number: **8752**

Examiner Number: **00000 /,**

Group Art Unit: **2812**

**IFW Madras**

Class/Subclass: **438/202.000**

Lost Case: **NO**

Interference Number:

Unmatched Petition: **NO**

L&R Code: Secrecy Code:1

Third Level Review: **NO**

Secrecy Order: **NO**

Oral Hearing: **NO**

Title of Invention: **METHOD FOR THE PRODUCTION OF A BIPOLAR TRANSISTOR  
COMPRISING AN IMPROVED BASE TERMINAL**

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| Bar Code | PALM Location | Location Date | Charge to Loc | Charge to Name | Employee Name | Location |
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